Errata 2\textsuperscript{th} edition

for the book

Digital Signal Processing with Field Programmable Gate Arrays

by

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Preface 2:
Page XI 5th bullet point:
Replace “request a copy via e-mail from Uwe.Meyer-Baese@ieee.org” with “from www.amazon.com”

Chapter 1:
Page 1 last line: Replace “Figgure” with “Figure”
Page 16 first line in sec. 1.4.1: Replace “seemed” with “seem”
Page 20 second line before Timing Estimates section: replace “rows” with “columns”
Page 21 Example 1.2, 6 line from the end: Replace $t_{cici}$ with $7 \times t_{cici}$
Page 22 Figure 1.14 low part of 74LS175: Replace “6 x FF” with “2 x FF”

Chapter 2:
Page 33 One’s Complement: Replace “same representation except” with “bit-by-bit complement representations including”
Page 36: first sentence after Example 2.1: Replace “nonezero” with “nonzero”
Page 62 Eq. (2.31): Replace “$Y_2Y_2$” with “$X_2Y_2$”
Page 90 Table second line: Replace “001” with “010”
Page 91 Eq. (2.48) and (2.49): Replace “$-2^b$” with “$-2^B$”
Page 92 Example 2.24: Replace “N=4-bit” with “B=4-bit”
Page 99 Fig. 2.38 3 times bottom assignments: Replace “-/+” with “+/-”

Chapter 3:
Page 115 Eq. (3.8): Replace “$d\Phi(\omega)$” with “$-d\Phi(\omega)$”
Page 115 Eq. (3.10): Add “k>0” to sum sign
Page 135 Example 3.8 first sentence: Replace “da3.mif” with “darom3.mif”
Page 142 first sentence after Example 3.10: Replace “archived” with “achieved”

Chapter 4:
Page 150 Eq. (4.3): Replace “$y[n-k]$” with “$y[n-l]$”
Page 152 item 4): Replace “unit circle” with “real axis”
Page 153 first sentence after Eq. (4.6): Replace “$|F(\omega)|^2$” with “$|F(\omega)|^2$”
Page 159 Example 4.2, coefficients A,B incorrect:
Remove “We will get … B=0.0001,… A=1.000…”
Page 166 Text before Eq. (4.16): Replace “0.25” with “0.75” and “1/4” with “3/4”
Page 167 Last Eq. in Example 4.5: Add “$72z^{-4}$” to nominator

Chapter 5:
Page 182 VHDL comments: Replace “m[0]= 127” with “m[0]=124” and “g[0]=127” with “g[0]=124”
Page 185 Figure lower signal path: Replace “$F_1(z)$” with “$F_0(z)$”
Page 185 sentence after Eq. (5.17): Replace “addition” with “delay”
Page 185 Eq. (5.19): Replace “$z^{-1}$” with “$-z^{-1}$”
Page 191 Fig. 5.17 caption: Add Fig. 5.17. “MatLab” Simulation of …
Page 192 VHDL code: Replace “unsigned” with “signed”
Page 196 Eq. (5.28): Replace “2N” with “2S”
Page 197 Eq. (5.32): Replace “j=2N+1” with “k=2S+1”
Page 199 VHDL code: Replace twice “2**14” with “2**13”; “2**13” with “2**12”; “2**12” with “2**11”; “unsigned” with “signed”
Page 219 Eq. (5.85): Use \[ G(z)\hat{G}(z) + H(z)\hat{H}(z) = F(z) - \hat{G}(-z)G(-z) = F(z) - F(-z) \]
Page 219 Eq (5.62): Replace “+” with “-”
Page 226 VHDL code: Replace “unsigned” with “signed”
Page 231 Eq. (5.83) replace “k^{2}/2” with “-k^{2}/2”

Chapter 6:
Page 241 Fig. 6.1: Replace “Tuckey” with “Tukey”
Page 244 Eq. (6.6): Use \[ x^* = \frac{1}{N} (W^*W)^\prime = \frac{1}{N} WX^* \]
Page 245 Table 6.1 second column: Replace “n=0” with “k=0” also “(x[n])” with “(x[n])”
Page 249 Eq. (6.8): Add 3 times subscript “N” to W
Page 251 Fig. 6.8 title: “2…” with “4…”
Page 252 Last equation: Replace \[
\begin{bmatrix}
20 \\
30 \\
40 \\
50 \\
60 \\
70
\end{bmatrix}
\]
with \[
\begin{bmatrix}
20 \\
40 \\
30 \\
70 \\
50 \\
60
\end{bmatrix}
\]
Page 254 VHDL code: Replace “unsigned” with “signed”
Page 258 Example 6.7:
\[
\begin{bmatrix}
X[0] \\
X[1] \\
X[2] \\
X[3] \\
X[4]
\end{bmatrix}
\]
with \[
\begin{bmatrix}
X[0] \\
X[4] \\
X[3] \\
X[2] \\
X[1]
\end{bmatrix}
\]
Page 262 third line: Replace “3 additions” with “6 additions”
Page 267 VHDL comments: Replace “i <= (c-s)” with “i <= (c+s)” also “(c+s)*y” with “(c-s)*y”
Page 267 last line: Replace “will increase” with “increases”
Page 269 VHDL code: Replace “unsigned” with “signed”

Chapter 8:
Page 376 Eq. (8.18); page 397 second equation; page 398 eq. (8.47); page 410 Eq. (8.66); Page 413 third equation; page 415 second equation: Replace “= =” with “=”
Appendix:
Page 451 line 17: Replace “[W3-1:0] y_out” with “[W4-1:0] y_out”
Page 489 table: switch VHDL page reference div_aegp<-> div_res, i.e. 67<->74